



#### IN THE UNITED ST AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Eiichi TAKAHASHI et al.

Application No.: 10/520,429

Docket No.: 122358

Filed: July 13, 2005

For:

DIGITAL CIRCUIT HAVING DELAY CIRCUIT FOR ADJUSTMENT OF CLOCK

SIGNAL TIMING

# REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records in accordance with the Preliminary Amendment filed concurrently herewith. A Supplemental Application Data Sheet is also attached.

Respectfully submitted,

James A. Oliff

Registration No. 27,075

Randi B. Isaacs

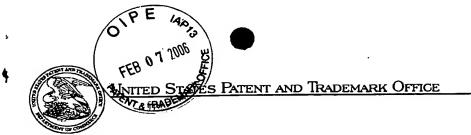
Registration No. 56,046

JAO:RBI/nxy

Date: February 7, 2006

OLIFF & BERRIDGE, PLC P.O. Box 19928 Alexandria, Virginia 22320 Telephone: (703) 836-6400

DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461



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**FILING OR 371** APPL NO. (c) DATE

ART UNIT

FIL FEE REC'D

ATTY.DOCKET NO

DRAWINGS TOT CLMS IND CLMS

10/520.429

07/13/2005

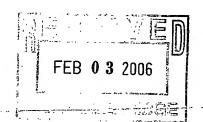
2631

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25944 OLIFF & BERRIDGE, PLC P.O. BOX 19928 **ALEXANDRIA, VA 22320** 



**CONFIRMATION NO. 5260** 

9

FILING RECEIPT



Date Mailed: 02/01/2006

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Eiichi Takahashi, Ibaraki, JAPAN; Tsukuba City Yuji Kasai, Ibaraki Pref., JAPAN; Tsukuba City Tetsuya Higuchi, Ibaraki Pref., JAPAN; Tsukuba City

Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/08648 07/08/2003

Foreign Applications

JAPAN 2002-200467 07/09/2002

Projected Publication Date: 05/11/2006

Non-Publication Request: No

Early Publication Request: No

\*\* SMALL ENTITY \*\*

**Title** 

Digital circuit having a delay circuit for clock signal timing adjustment bightal circuit having delay circuit for adjustment of clock signal timing

## **Preliminary Class**

375

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# OLIFF & BERRIDGE, PLC

ATTORNEYS AT LAW

## **Supplemental Application Data Sheet**

## **Applicant Information**

Applicant Authority type::

Inventor

Primary Citizenship Country::

Japan

Status::

**Full Capacity** 

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Family Name::

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Country of Residence::

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Applicant Authority Type::

Inventor

Primary Citizenship Country::

Japan Full Capacity

Status::

Yuji

Given Name::

ı ujı

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Country of Residence::

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Family Name::

HIGUCHI

City of Residence::

Tsukuba-shiTsukuba City

Country of Residence::

Japan

## **Correspondence Information**

Correspondence Customer Number::

25944

## **Application Information**

Application Type::

**National Phase** 

Subject Matter::

Utility

CD-ROM or CD-R::

None

Title::

DIGITAL CIRCUIT HAVING DELAY

CIRCUIT FOR ADJUSTMENT OF CLOCK

SIGNAL TIMING

Attorney Docket Number::

122358

Suggested Drawing Figure::

1

**Total Drawing Sheets::** 

19

Small Entity::

No

# **Representative Information**

Attorneys associated with Customer No. 25944.

Domestic Priority Information			
Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application is a	National Stage of	PCT/JP03/08648	07/08/03
Foreign Priority Information			
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	2002-200467	07/09/02	Yes
Assignee Information			
Assignee Name::		National Institute of Advanced Industrial	
		Science and Technology	
Street of mailing address::		3-1, Kasumigaseki 1-chome, Chiyoda-ku	
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Country of mailing address::		Japan	
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